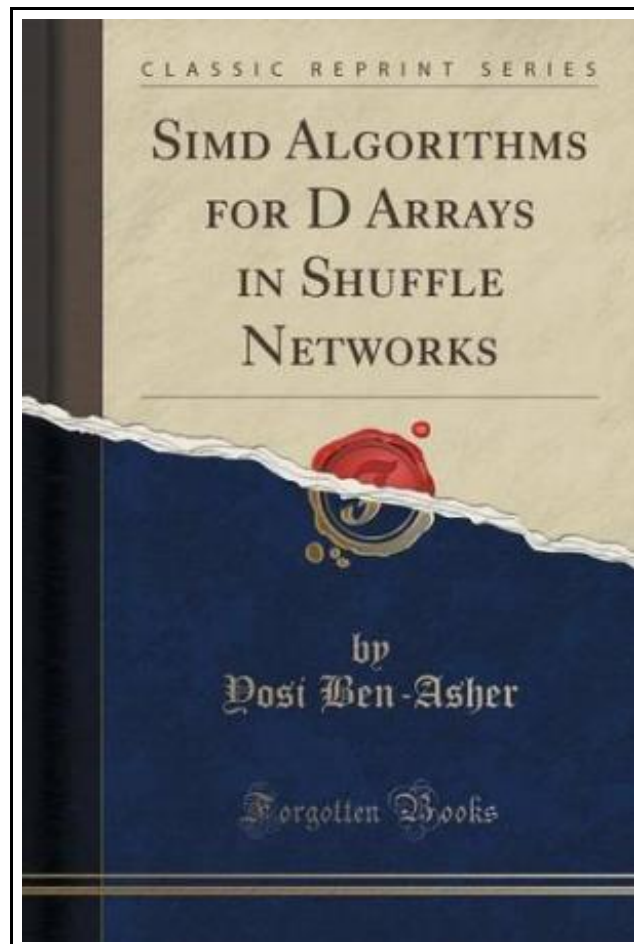


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Forgotten Books, United States, 2015. Paperback. Book Condition: New. 229 x 152 mm. Language: English . Brand New Book ***** Print on Demand *****.Excerpt from *Simd Algorithms for D Arrays in Shuffle Networks* 1. Introduction and the Model Schwartz [1] introduced the idea of a parallel processor based on the shuffle connections (2); reviewed various bask of SIMD algorithms for such an ensemble of processors, and analyzed their asymptotk time complexities. However, these algorithms arc designed for the 1-D ease, where iV, the size of the problem is restricted to be P, the number of processors. In reallity. the general ease where $N > P$ is much more likely to appear. In this cast each processor stores $L = S/P$ data elements in its local memory. This data structure is referred to ax a 2-D array of P columns and L rows, where the Tih column corresponds to the local memory of the processor PE/. Our algorithms correspond to a SIMD Perfect Shuffle connected machine (PS) [1]. Fo: simplicity, although it is not crucial, we assume that P is a power of two. The processors arc ordered left to right and arc numbered 0 - P-1 accordingly, so we denote the; th processor PE; Subgroups of processors arc referred to in a natural manner: left half, right half. odd. even etc. There arc three kinds of communication steps: shuffle (o). unshufflc (-7-1) and exchange (X) and a processor internal (fixed-size) computation. All of these operations take one time step. In any parallel network machine we have two possible working modes: One in which each processor computes a local result of a sequential computation using its local data. Then an inter-processor global algorithm is carried out to compute the final result. In other words, this mode involves separate sequential wotk by PE...



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